

650V 37mohm Super-Junction Power MOSFET  
AK3S65N370WMF

Description:

This SJ device integrated with fast-recovery diode provides good FOM factor, EMI Friendly for customer application.

Features:

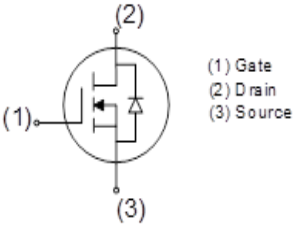
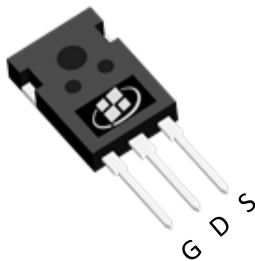
- RoHS compliant
- Halogen-free
- 100% UIS tested
- EMI-Friendly

Applications:

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- LED Light

Key Performance Parameters:

| Parameter                               | Value | Unit |
|-----------------------------------------|-------|------|
| $V_{DS}$                                | 650   | V    |
| $R_{DS(ON)}, max @V_{GS} = 10\text{ V}$ | 37    | mΩ   |
| $I_D$                                   | 65    | A    |



Ordering Information:

| Ordering Code | Package Type | Marking Code | Form | Packing     |
|---------------|--------------|--------------|------|-------------|
| AK3S65N370WMF | TO-247-3L    | 3S65N370WMF  | Tube | 300 per box |

**Maximum Ratings** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

| Symbol         | Parameter                                                                   | Value       | Units            |
|----------------|-----------------------------------------------------------------------------|-------------|------------------|
| $V_{DS}$       | Drain - Source Voltage                                                      | 650         | V                |
| $I_D$          | Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ ) <sup>(Note 1)</sup> | 65          | A                |
|                | Drain Current - Continuous ( $T_C = 100^\circ\text{C}$ )                    | 41          | A                |
| $I_{DM}$       | Drain Current - Pulsed <sup>(Note 2)</sup>                                  | 220         | A                |
| $V_{GS}$       | Gate - Source Voltage                                                       | $\pm 30$    | V                |
| $E_{AS}$       | Single Pulsed Avalanche Energy <sup>(Note 3)</sup>                          | 390         | mJ               |
| dv/dt          | MOSFET dv/dt ruggedness                                                     | 100         | V/ns             |
|                | Reverse diode dv/dt                                                         | 75          | V/ns             |
| $P_D$          | Power Dissipation ( $T_C = 25^\circ\text{C}$ )                              | 390         | W                |
| $T_J, T_{STG}$ | Operating and Storage Temperature Range                                     | -55 to +150 | $^\circ\text{C}$ |

**Thermal Characteristics**

| Symbol          | Parameter                                                                     | Value | Units              |
|-----------------|-------------------------------------------------------------------------------|-------|--------------------|
| $R_{\theta JC}$ | Thermal Resistance, Junction - to - Case, Steady-State                        | 0.32  | $^\circ\text{C/W}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction - to - Ambient, Steady State <sup>(Note 4)</sup> | 35    | $^\circ\text{C/W}$ |

**Notes:**

1. The max drain current rating limited by package and maximum junction temperature
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3.  $L = 10.8 \text{ mH}$ ,  $V_{DD} = 150 \text{ V}$ ,  $I_{AS} = 8.5 \text{ A}$ ,  $R_g = 50 \Omega$ , Starting  $T_J = 25^\circ\text{C}$ , guarantee by design
4. Mount on minimum PCB layout

**Electrical Characteristics** ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

| Symbol                                                   | Parameter                                       | Test Conditions                                                                                  | Min | Typ  | Max  | Units |
|----------------------------------------------------------|-------------------------------------------------|--------------------------------------------------------------------------------------------------|-----|------|------|-------|
| Static Characteristics                                   |                                                 |                                                                                                  |     |      |      |       |
| V <sub>(BR)DSS</sub>                                     | Drain - Source Breakdown Voltage                | V <sub>GS</sub> = 0 V, I <sub>D</sub> =1 mA                                                      | 650 |      |      | V     |
| I <sub>DSS</sub>                                         | Zero Gate Voltage Drain Current                 | V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V                                                   |     |      | 10   | μA    |
| I <sub>GSS</sub>                                         | Gate Leakage Current                            | V <sub>GS</sub> = ± 30 V, V <sub>DS</sub> = 0 V                                                  |     |      | ±100 | nA    |
| V <sub>GS(th)</sub>                                      | Gate Threshold voltage                          | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA                                      | 3   | 4    | 5    | V     |
| R <sub>DS(ON)</sub>                                      | Drain - Source on - state resistance            | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 32 A                                                    |     | 29   | 37   | mΩ    |
| Dynamic Characteristics                                  |                                                 |                                                                                                  |     |      |      |       |
| C <sub>iss</sub>                                         | Input Capacitance                               | V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V,<br>f = 1 MHz                                     |     | 7185 |      | pF    |
| C <sub>oss</sub>                                         | Output Capacitance                              |                                                                                                  |     | 103  |      | pF    |
| C <sub>rss</sub>                                         | Reverse Transfer Capacitance                    |                                                                                                  |     | 16   |      | pF    |
| C <sub>o(er)</sub>                                       | Effective output capacitance, energy related    | V <sub>DS</sub> = 0...400 V, V <sub>GS</sub> = 0 V                                               |     | 207  |      | pF    |
| C <sub>o(tr)</sub>                                       | Effective output capacitance, time related      | V <sub>DS</sub> = 0...400 V, V <sub>GS</sub> = 0 V,<br>I <sub>D</sub> =constant                  |     | 1743 |      | pF    |
| R <sub>g</sub>                                           | Gate Resistance                                 | f = 1 MHz                                                                                        |     | 3.6  |      | Ω     |
| Switching Characteristics                                |                                                 |                                                                                                  |     |      |      |       |
| t <sub>d(on)</sub>                                       | Turn On Delay Time                              | V <sub>DD</sub> = 480 V , I <sub>D</sub> = 32 A<br>V <sub>GS</sub> = 10 V, R <sub>G</sub> = 24 Ω |     | 131  |      | ns    |
| t <sub>r</sub>                                           | Rise Time                                       |                                                                                                  |     | 84   |      | ns    |
| t <sub>d(off)</sub>                                      | Turn Off Delay Time                             |                                                                                                  |     | 341  |      | ns    |
| t <sub>f</sub>                                           | Fall Time                                       |                                                                                                  |     | 60   |      | ns    |
| Q <sub>g</sub>                                           | Total Gate Charge                               | V <sub>DD</sub> = 400 V , I <sub>D</sub> = 32 A,<br>V <sub>GS</sub> = 10 V                       |     | 160  |      | nC    |
| Q <sub>gs</sub>                                          | Gate - Source Charge                            |                                                                                                  |     | 42   |      | nC    |
| Q <sub>gd</sub>                                          | Gate - Drain Charge                             |                                                                                                  |     | 67   |      | nC    |
| Drain - Source Diode Characteristics and Maximum Ratings |                                                 |                                                                                                  |     |      |      |       |
| I <sub>S</sub>                                           | Maximum Continuous Body - Diode Forward Current |                                                                                                  |     |      | 65   | A     |
| I <sub>SM</sub>                                          | Maximum Pulsed Body - Diode Forward Current     |                                                                                                  |     |      | 220  | A     |
| V <sub>SD</sub>                                          | Diode Forward Voltage                           | V <sub>GS</sub> = 0 V, I <sub>S</sub> = 32 A                                                     |     | 0.9  |      | V     |
| t <sub>rr</sub>                                          | Reverse recovery time                           | V <sub>DD</sub> = 400 V, I <sub>D</sub> = 32 A,<br>di/dt = 100 A/μS                              |     | 150  |      | ns    |
| Q <sub>rr</sub>                                          | Reverse recovery charge                         |                                                                                                  |     | 1.3  |      | μC    |
| I <sub>rrm</sub>                                         | Peak Reverse Recovery Current                   |                                                                                                  |     | 17   |      | A     |

Electrical Characteristics Diagrams

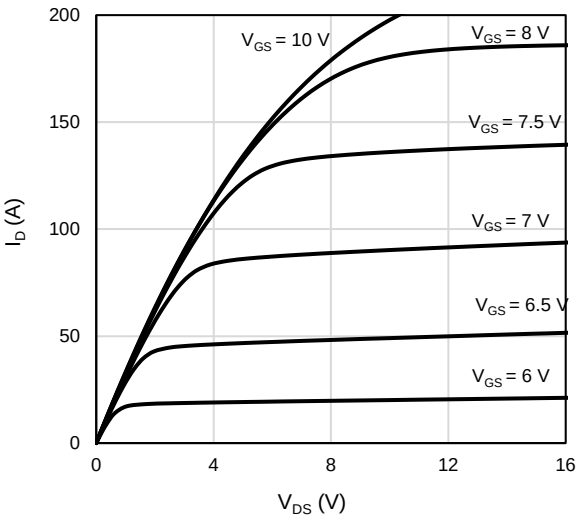


Figure 1: On-Region Characteristics

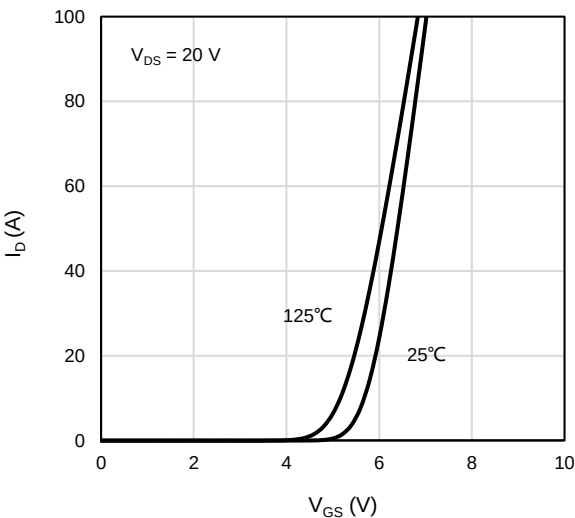


Figure 2: Transfer Characteristics

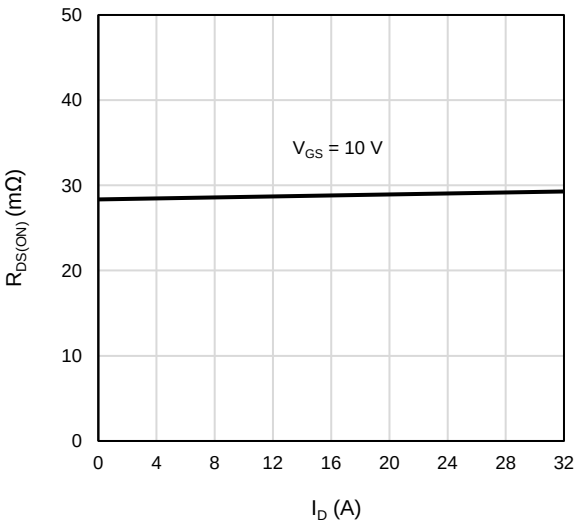


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

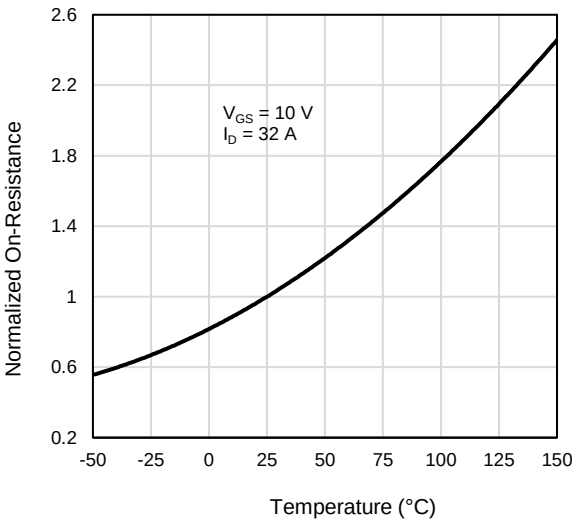


Figure 4: On-Resistance vs. Junction Temperature

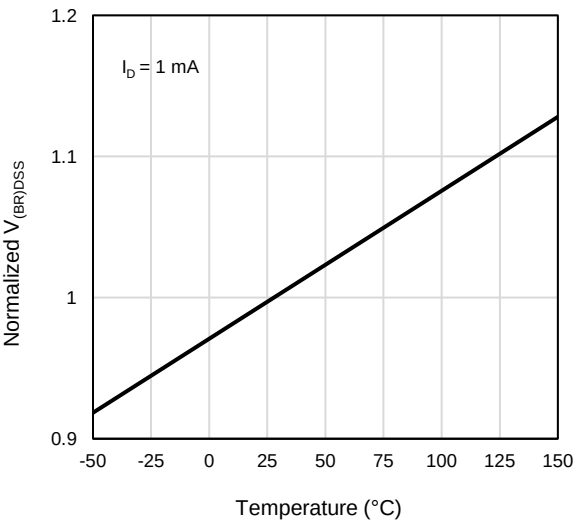


Figure 5: Breakdown Voltage vs. Junction Temperature

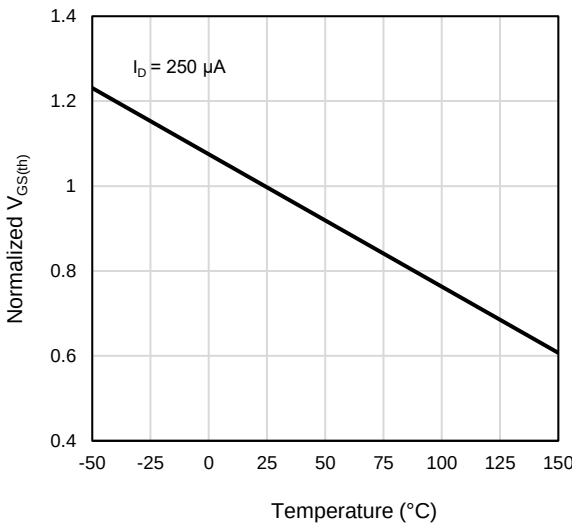


Figure 6: Threshold Voltage vs. Junction Temperature

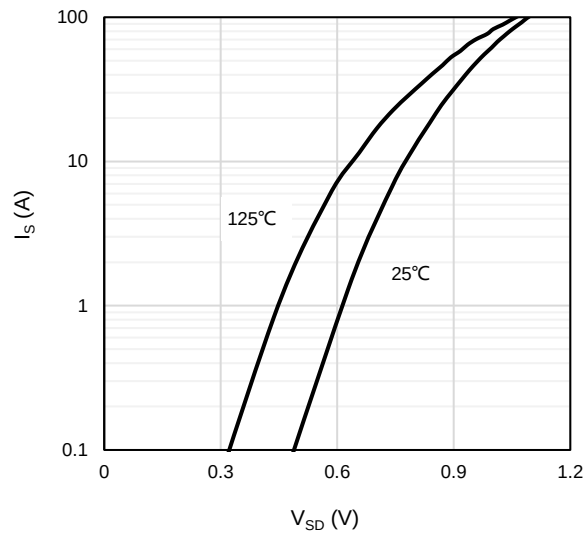


Figure 7: Body-Diode Characteristics

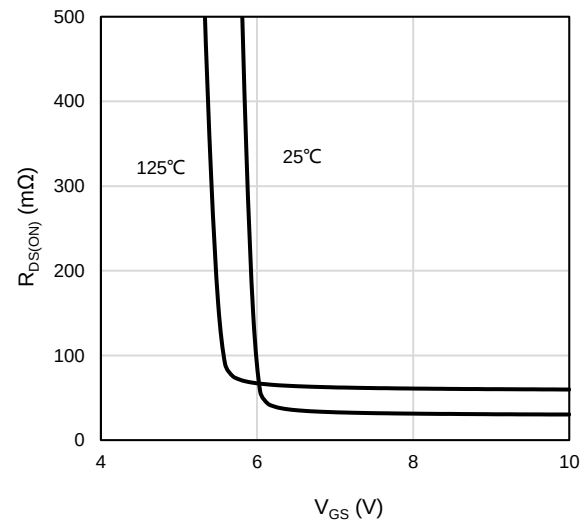


Figure 8: On-Resistance vs. Gate-Source Voltage

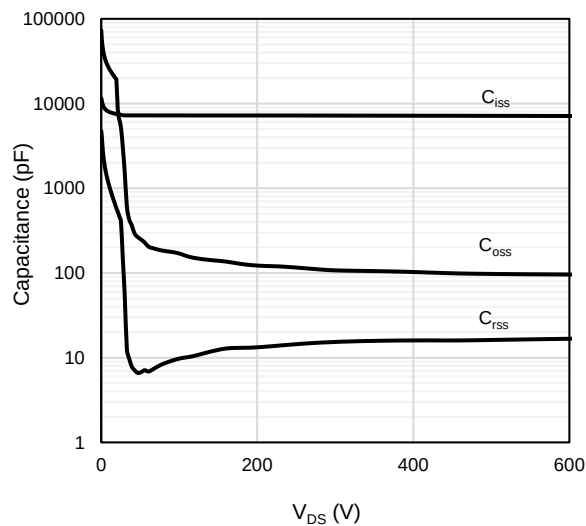


Figure 9: Capacitance Characteristics

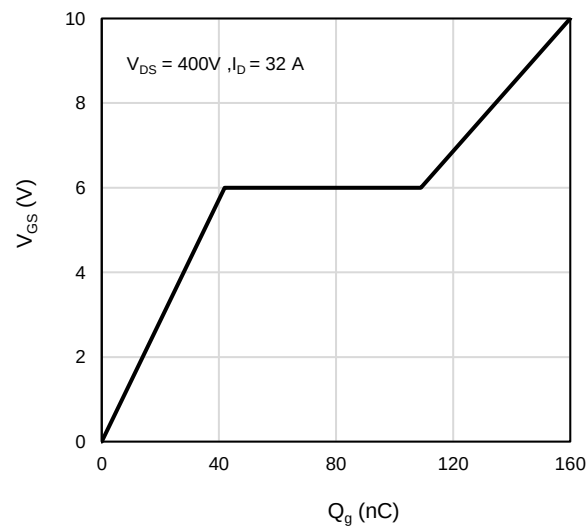


Figure 10: Gate-Charge Characteristics

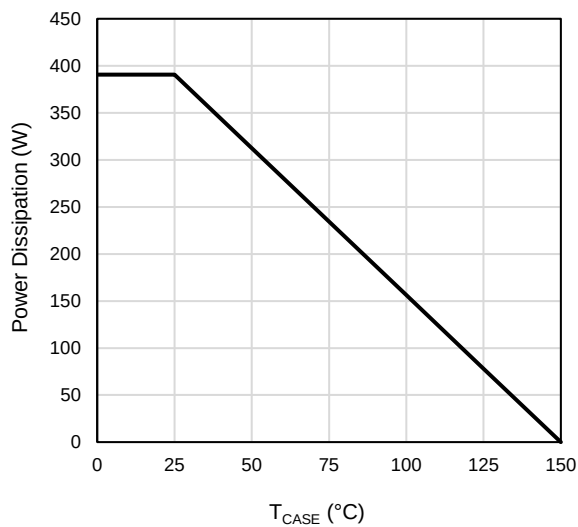


Figure 11: Power De-rating

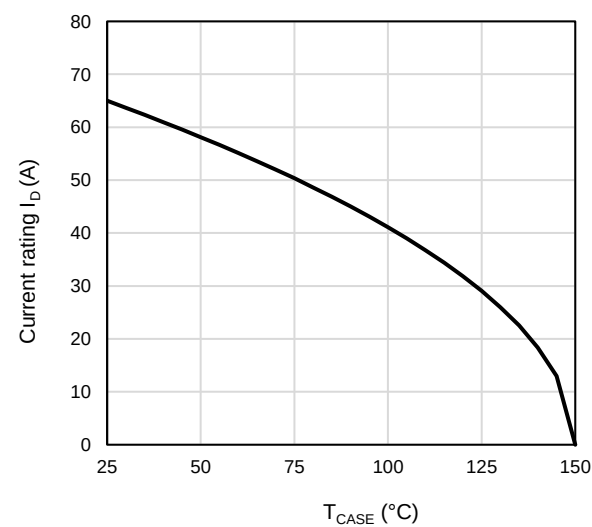


Figure 12: Current De-rating

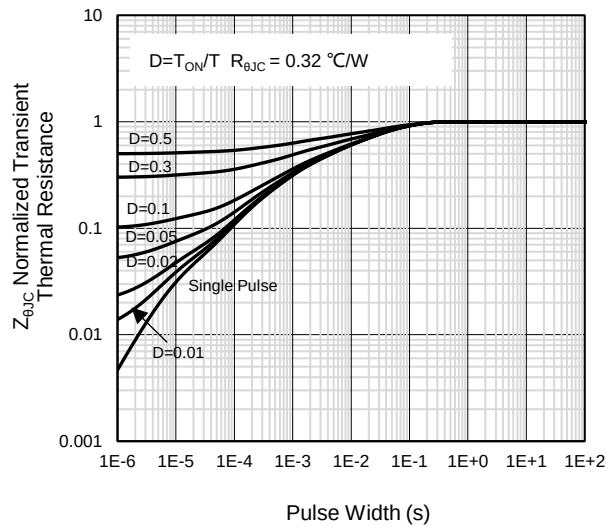


Figure 13: Normalized Maximum Transient Thermal Impedance

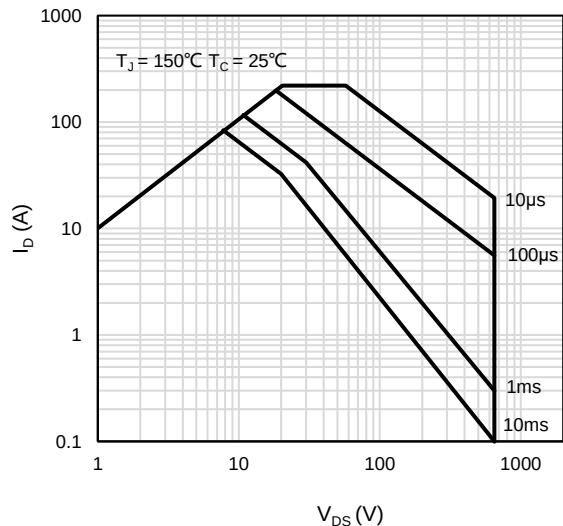
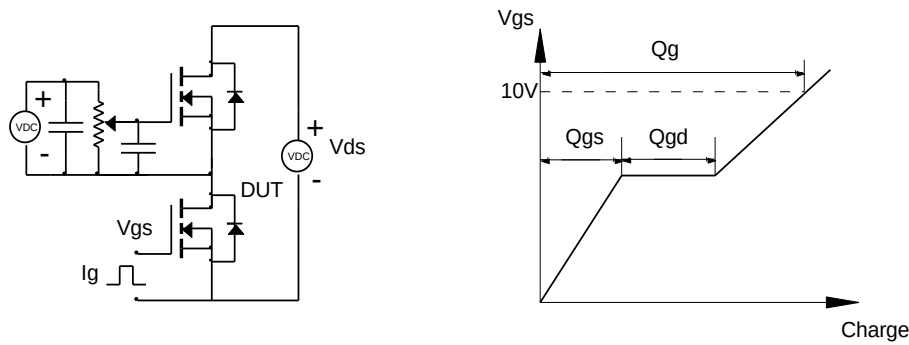


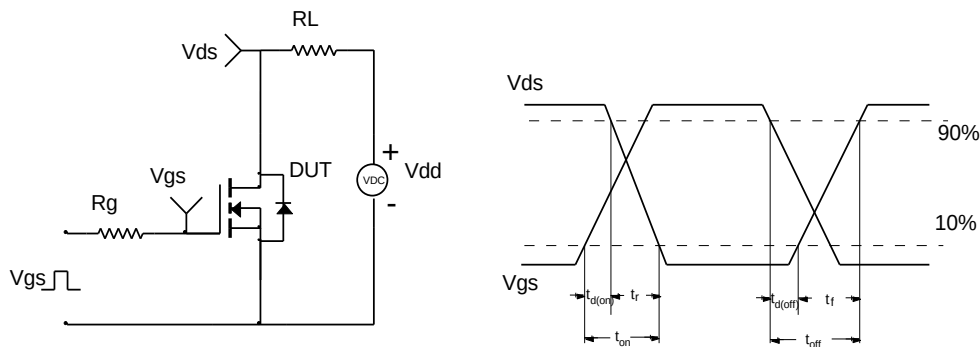
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

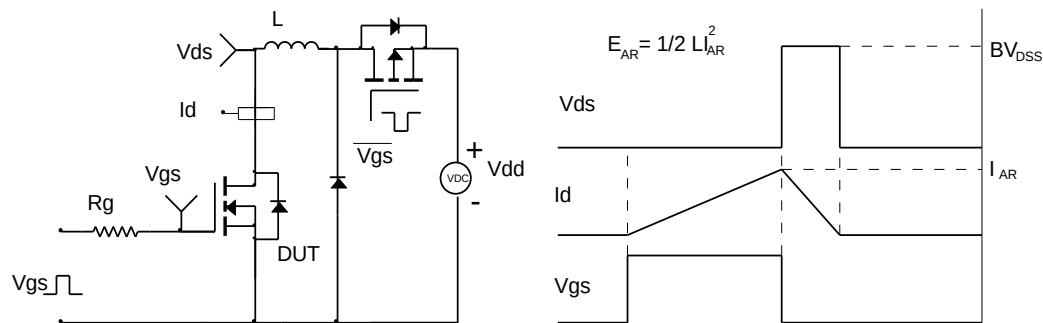
Gate Charge Test Circuit & Waveform



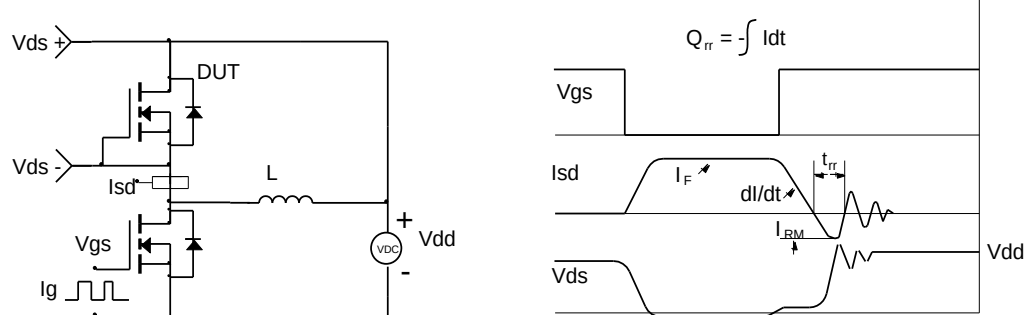
Resistive Switching Test Circuit & Waveforms



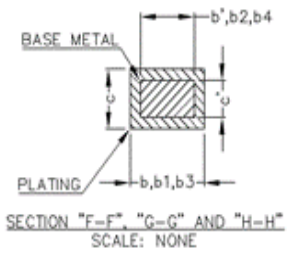
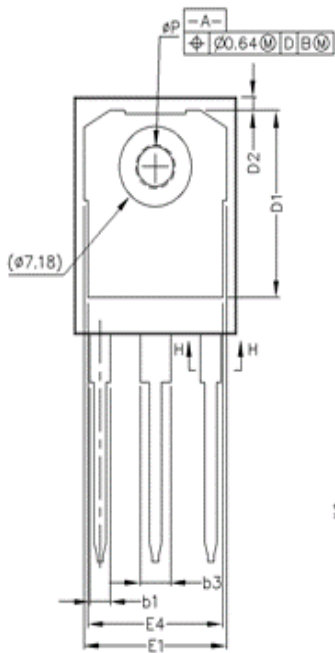
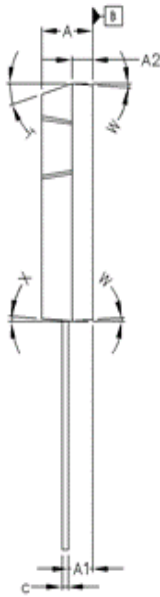
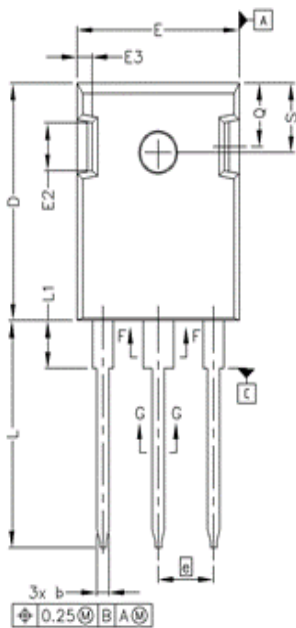
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outlines



| SYMBOL | MIN        | MAX   |
|--------|------------|-------|
| A      | 4.83       | 5.21  |
| A1     | 2.29       | 2.54  |
| A2     | 1.91       | 2.16  |
| b'     | 1.07       | 1.28  |
| b      | 1.07       | 1.33  |
| b1     | 1.91       | 2.41  |
| b2     | 1.91       | 2.16  |
| b3     | 2.87       | 3.38  |
| b4     | 2.87       | 3.13  |
| c'     | 0.55       | 0.65  |
| c      | 0.55       | 0.68  |
| D      | 20.80      | 21.10 |
| D1     | 16.25      | 17.65 |
| D2     | 0.95       | 1.25  |
| E      | 15.75      | 16.13 |
| E1     | 13.10      | 14.15 |
| E2     | 3.68       | 5.10  |
| E3     | 1.00       | 1.90  |
| E4     | 12.38      | 13.43 |
| e      | 5.44 BSC   |       |
| N      | 3          |       |
| L      | 19.81      | 20.32 |
| L1     | 4.10       | 4.40  |
| P      | 3.51       | 3.65  |
| Q      | 5.49       | 6.00  |
| S      | 6.04       | 6.30  |
| T      | 17.5 ° REF |       |
| W      | 3.5 ° REF  |       |
| X      | 4 ° REF    |       |



## Marking Information



3S65N370WMF  
KYWWZZZ

Note:

3S65N370WMF = Product Name Code

KYWWZZZ = Date code

Contact ALKAIDSEMI sales for detail information

Revision History

| Revision | Released | Remark          |
|----------|----------|-----------------|
| Rev.1.0  | 2023     | initial release |

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior